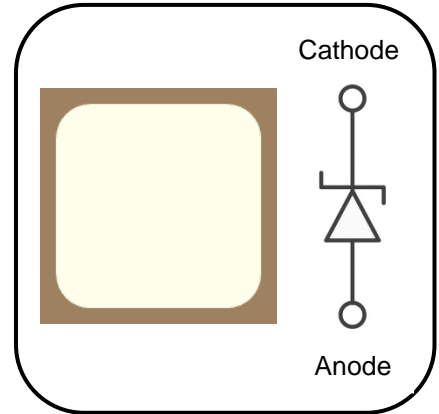


4th Generation 650V/10A SiC Schottky Barrier Diode

Features

- AEC-Q101 qualified
- Revolutionary semiconductor material - Silicon Carbide (SiC)
- No reverse recovery
- High-speed switching performance
- Temperature-independent switching behavior
- System cost / size savings due to reduced cooling requirements
- Junction temperature range from -55°C to 175°C
- RoHS compliant



Potential Applications

- Industrial power supplies: Industrial UPS
- Battery chargers
- Solar inverters
- Switch mode power supplies



Description

The ADS065J010B4 SiC Schottky Barrier Diode (SBD) has been developed using Sanan’s advanced 4th generation SiC SBD technology with the highest performance and reliability. It registers higher efficiency, higher operation temperature and lower loss and can be operated at higher frequency than Si-based solutions. As to the Schottky structure, it shows no recovery at turn-off and allows a low leakage current with reverse voltage up to 650V. It can contribute to system miniaturization and achieve lightweight system design. Using RoHS compliant components, it is qualified for use in industrial application.

Product Specifications

Device	V_{RRM}	$I_F (135^\circ C)$	$V_F (25^\circ C)$	Q_C
ADS065J010B4	650V	15A	1.29V	32nC

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Table 1. Maximum Ratings

Parameter	Symbol	Value	Unit	Test conditions
Repetitive peak reverse voltage	V_{RRM}	650	V	$T_C = 25^{\circ}C$
Surge peak reverse voltage	V_{RSM}	650		$T_C = 25^{\circ}C$
DC reverse voltage	V_{DC}	650		$T_C = 25^{\circ}C$
Continuous forward current	I_F	31	A	$T_C = 25^{\circ}C$
		15		$T_C = 135^{\circ}C$
		10		$T_C = 155^{\circ}C$
Surge non-repetitive forward current	I_{FSM}	80	A	$T_C = 25^{\circ}C$, $t_p = 10ms$, half sine pulse
Repetitive peak forward current	I_{FRM}	57	A	$T_C = 25^{\circ}C$, $t_p = 10ms$, half sine wave $D = 0.1$
i^2t value	$\int i^2 dt$	32	A^2s	$T_C = 25^{\circ}C$, $t_p = 10ms$
Operating junction temperature	T_j	-55~175	$^{\circ}C$	
Storage temperature	T_{stg}	-55~175	$^{\circ}C$	

Table 2. Thermal Resistance

Parameter	Symbol	Values			Unit	Test condition
		Min.	Typ.	Max.		
Thermal resistance from junction to case	$R_{th(j-c)}$	/	1.25	/	$^{\circ}C/W$	

*Thermal Resistance is collected in T2PAK

Table 3. Static Electrical Characteristics

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
DC blocking voltage	V_{DC}	650	/	/	V	$I_R = 100 \mu A$
Forward voltage	V_F	/	1.29	1.45	V	$I_F = 10A, T_j = 25^\circ C$
		/	1.46	1.55		$I_F = 10A, T_j = 175^\circ C$
Reverse current	I_R	/	1	50	μA	$V_R = 650V, T_j = 25^\circ C$
		/	10	300		$V_R = 650V, T_j = 175^\circ C$

Table 4. Dynamic Electrical Characteristics

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Total capacitance	C	/	716	/	pF	$V_R = 0V, f = 1MHz$
		/	61	/		$V_R = 200V, f = 1MHz$
		/	48	/		$V_R = 400V, f = 1MHz$
Total capacitive charge	Q_C	/	32	/	nC	$V_R = 400V$
Capacitance stored energy	E_C	/	4.7	/	μJ	$V_R = 400V$

Electrical Characteristic Diagrams

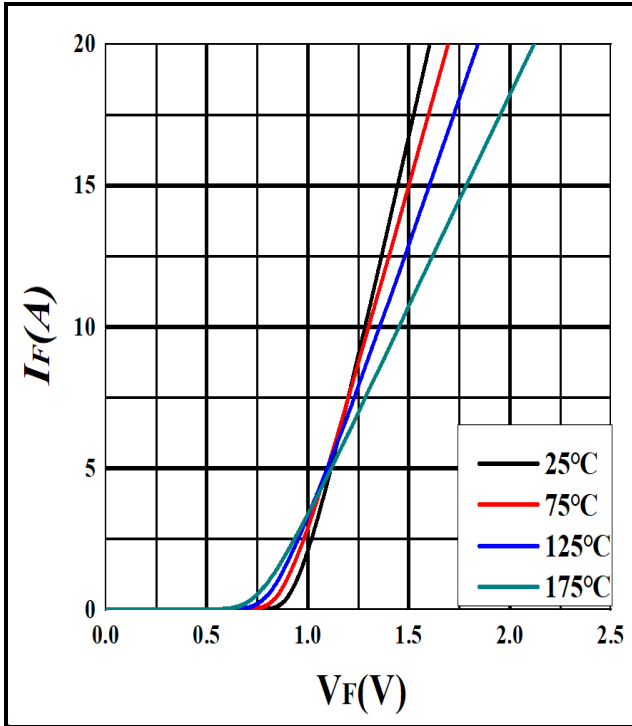


Figure 1. Forward characteristics

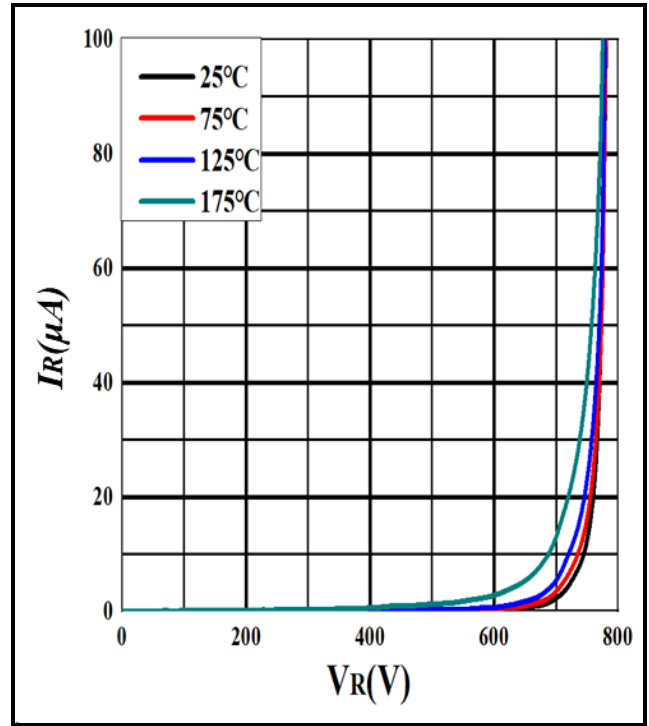


Figure 2. Reverse characteristics

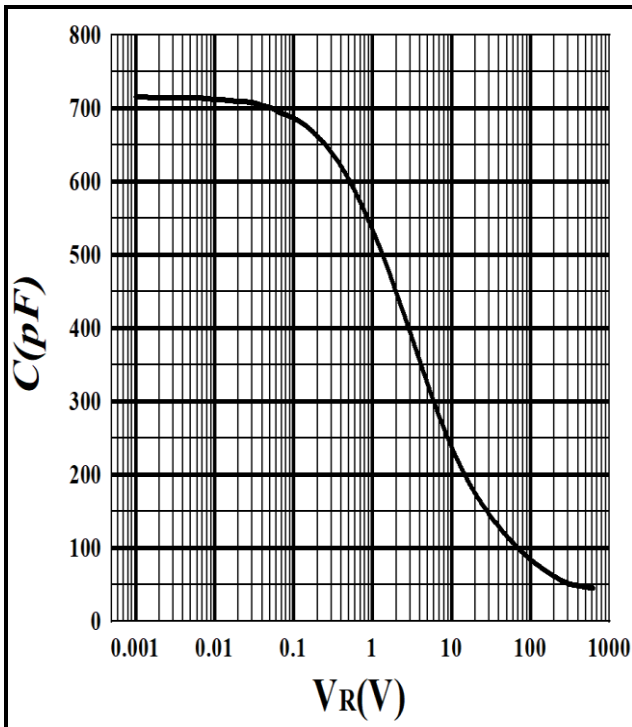


Figure 3. Capacitance vs. reverse voltage

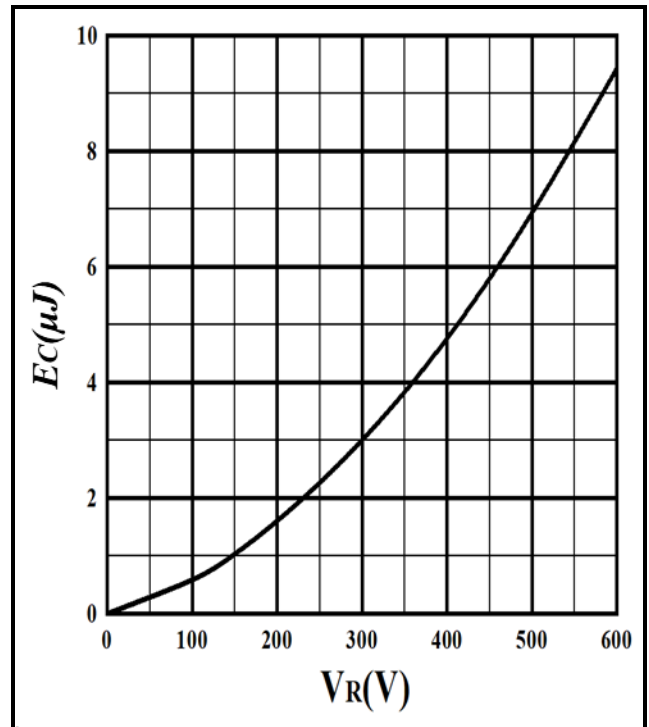


Figure 4. Capacitance stored energy

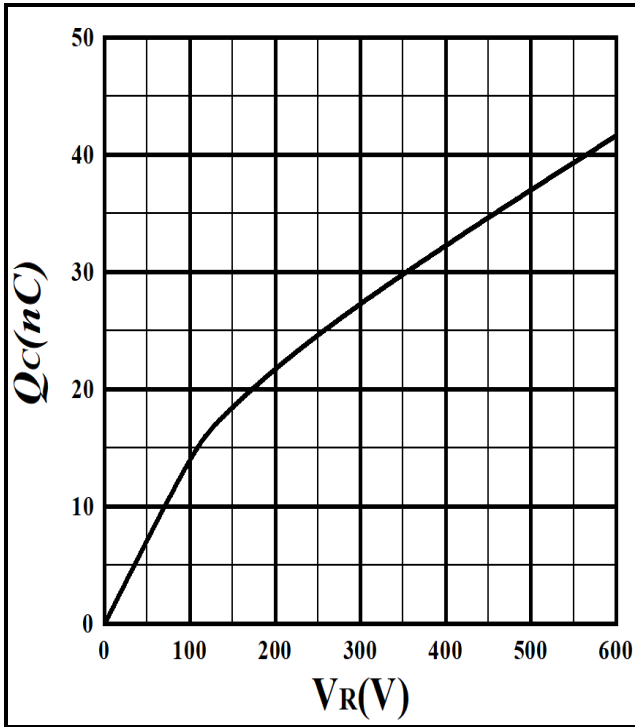


Figure 5. Total capacitance charge vs. reverse voltage

Ordering Information

Part Number	ADS065J010B4
Package	Bare Die
Packing Method	Wafer
RoHS	Yes

Important Notices – Read Carefully

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