

Datasheet

SemiconductorSiCPolytype4HDiameter150 mm



Table 1. EPITAXY Specifications						
Parameter		Specification	Unit	Test Method		
Dopant		Nitrogen				
Epi-Layer1 (buffer) Doping Concentration	Range	1 E17 ~ 2 E18	At/cm ³			
	Tolerance	± 30	%	Tolerance calculation method: (Extreme value -Target value) /Target value		
Epi-Layer1 (buffer) Thickness	Range	0.5 ~ 1	um			
	Tolerance	± 10	%	Tolerance calculation method: (Extreme value -Target value) /Target value		
Epi-Layer2 Doping Concentration	Range	5E14 ~ 5E17	At/cm ³	Measured with 5 mm edge exclusion by MCV		
	Tolerance	± 20	%	1.Tolerance calculation method: (Extreme value -Target value) /Target value 2. All measured points within		
	Uniformity	≤ 8	%	Uniformity calculation method: Standard deviation value/Mean value		
Epi-Layer2 Thickness	Range	3 ~ 30	um	Measured with 5 mm edge exclusion by FTIR Including buffer layer thickness		
	Tolerance	± 5	%	1.Tolerance calculation method: (Extreme value -Target value) /Target value 2.All measured points within		
	Uniformity	≤ 5	%	Uniformity calculation method: Standard deviation value/Mean value		
Total Thickness Variation (TTV)		≤ 8	um	Measured by FRT		
Warp		≤ 70	um	Measured by FRT		
Bow		± 60	um	Measured by FRT		
Local Thickness Variation (LTV)		≤ 3.5	um	Measured by FRT, 10mm*10mm site size		
Electrically Active Defects		≤ 1	cm ⁻²	Measured by Candela 8520 or Lasertec Sica88 Defects includes :Triangles, Downfalls, Carrots		



Datasheet

Scratches (Front side surface)	Cumulative length ≤ 1X wafer diameter, Cumulative quantity ≤ 5	mm	Measured by Candela 8520 or Lasertec Sica88
Surface Roughness (Ra)	< 0.5	nm	20 μm *20 μm scan by AFM
Die yield	≥ 90	%	1. Measured by Candela 8520 or Lasertec Sica88 with 3 mm edge exclusion 2. Die size: 3mm*3 mm 3. Defects includes:Triangles, Downfalls, Scratches, Carrots
Surface Metal Contamination	<1 E11	Atoms/ cm ²	Na/K/Ca/Fe/Ni/Cu/Zn/Au/Ag/Al/Ga/ Hg/As/Pt), Measured by ICP-MS
Backside Coating	width < 3 mm		Inspection performed using unaided eye under bright diffuse light
Backside Stains	area < 2 * 2 cm ²		Inspection performed using unaided eye under bright diffuse light

Important Notices - Read Carefully

Before you use our products, you are requested to carefully read this document and fully understand its contents. Hunan Sanan Semiconductor Co., Ltd. (Hunan Sanan) shall not be in any way responsible or liable for failure, malfunction or accident arising from the use of Hunan Sanan's products.

Hunan Sanan Semiconductor Datasheets are subject to change. Information presented in this document is from the characterization of engineering lots. Hunan Sanan reserves the right to change limits, test conditions, and dimensions without notice. Information contained in this document are typical values and shall in no event be regarded as a guarantee of characteristics. With respect to any information regarding the application of the product, Hunan Sanan hereby disclaims all warranties and liabilities of any kind. The information in this document is exclusively for trained technical staff. It is the responsibility of the customer's technical department to decide the suitability of the product in the customer's application and Hunan Sanan assumes no responsibility or liability whatsoever for the use of the information contained in this document.

In case there is any clause in this document or in any other documents which is contradictory to this clause, this clause shall prevail. This clause shall survive after termination of this document.

Warning

Due to technical requirements, Hunan Sanan products may contain dangerous substances. For detailed information about the substance(s), please contact the Hunan Sanan office. Hunan Sanan bears no responsibility for any damage whatsoever due to the substance(s) used in Hunan Sanan products.

Contact info

Website: https://www.sanan-semiconductor.com/