

3rd Generation 1200V/3A SiC Schottky Barrier Diode

Features

- Revolutionary semiconductor material - Silicon Carbide (SiC)
- No reverse recovery
- High-speed switching performance
- Temperature-independent switching behavior
- System cost / size savings due to reduced cooling requirements
- Junction temperature range from -55°C to 175°C
- RoHS compliant



Package Type: TO-252-2L

Potential Applications

- Industrial power supplies: Industrial UPS
- Battery chargers
- Solar inverters
- Switch mode power supplies



Description

The SDS120J003D3 SiC Schottky Barrier Diode (SBD) has been developed using Sanan’s advanced 3rd generation SiC SBD technology with the highest performance and reliability. It registers higher efficiency, higher operation temperature and lower loss and can be operated at higher frequency than Si-based solutions. As to the Schottky structure, it shows no recovery at turn-off and allows a low leakage current with reverse voltage up to 1200V. It can contribute to system miniaturization and achieve lightweight system design. Using RoHS compliant components, it is qualified for use in industrial application.

Product Specifications

Device	V _{RRM}	I _F (135°C)	V _F (25°C)	Q _c	Marking
SDS120J003D3	1200V	6.8A	1.35V	17nC	DS120003D3

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Table 1. Maximum Ratings

(T_c = 25°C, unless otherwise specified)

Parameter	Symbol	Value	Unit	Test conditions
Repetitive peak reverse voltage	V _{RRM}	1200	V	T _C = 25°C
Surge peak reverse voltage	V _{RSM}	1200		T _C = 25°C
DC reverse voltage	V _{DC}	1200		T _C = 25°C
Continuous forward current	I _F	14	A	T _C = 25°C
		6.8		T _C = 135°C
		3.0		T _C = 155°C
Surge non-repetitive forward current	I _{FSM}	38	A	T _C = 25°C, t _p = 10ms, half sine pulse
Non-repetitive peak forward current	I_{F,Max}	270	A	T_C = 25°C, t_p = 10μs, pulse
Surge repetitive forward current	I _{FRM}	20	A	T _C = 25°C, t _p = 10ms, half sine wave D = 0.1
Power dissipation	P _{tot}	96	W	T _C = 25°C
i ² t value	∫i ² dt	3.6	A ² s	T _C = 25°C, t _p = 10ms
Operating junction temperature	T _j	-55~175	°C	
Storage temperature	T _{stg}	-55~175	°C	

Table 2. Thermal Resistance

Parameter	Symbol	Values			Unit	Test condition
		Min.	Typ.	Max.		
Thermal resistance from junction to case	R _{th(j-c)}	/	1.55	/	°C/W	

Table 3. Static Electrical Characteristics

(T_j = 25°C, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
DC blocking voltage	V _{DC}	1200	/	/	V	I _R = 100 μA
Forward voltage	V _F	/	1.35	1.50	V	I _F = 3A, T _j = 25°C
		/	1.85	2.20		I _F = 3A, T _j = 175°C
Reverse current	I _R	/	1	12	μA	V _R = 1200V, T _j = 25°C
		/	10	48		V _R = 1200V, T _j = 175°C

Table 4. Dynamic Electrical Characteristics

(T_j = 25°C, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Total capacitance	C	/	238	/	pF	V _R = 0V, f = 1MHz
		/	16	/		V _R = 400V, f = 1MHz
		/	12	/		V _R = 800V, f = 1MHz
Total capacitive charge	Q _C	/	17	/	nC	V _R = 800V
Capacitance stored energy	E _C	/	4.9	/	μJ	V _R = 800V

Electrical Characteristic Diagrams

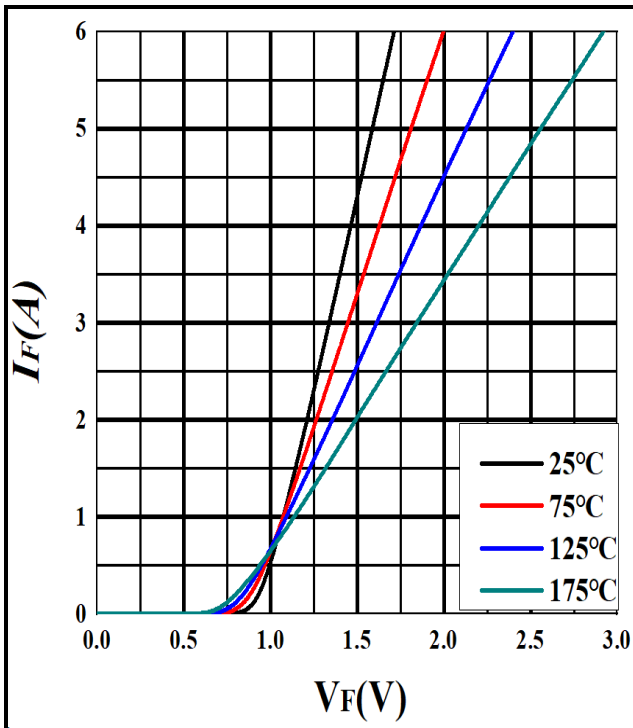


Figure 1. Forward characteristics

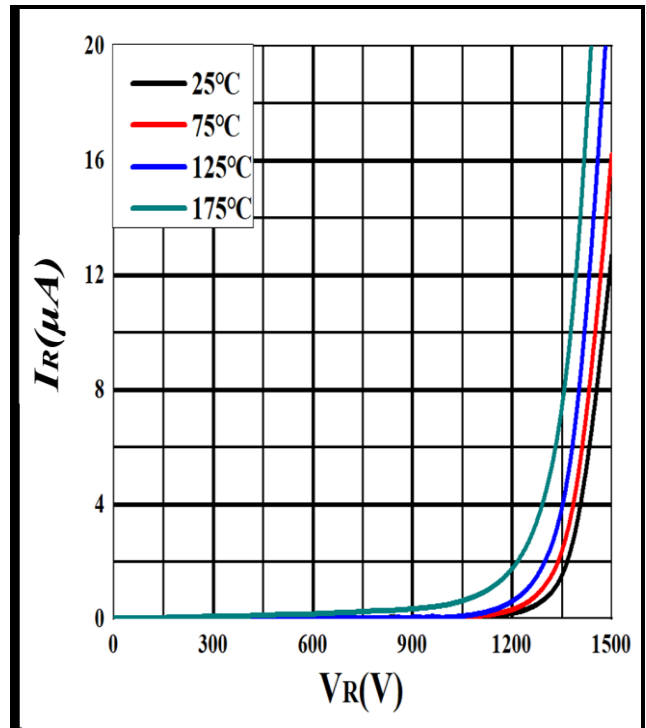


Figure 2. Reverse characteristics

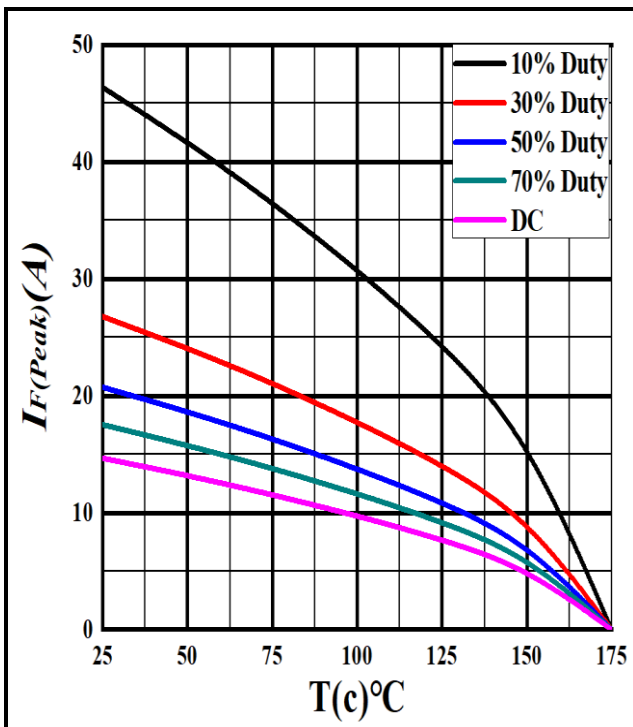


Figure 3. Current derating

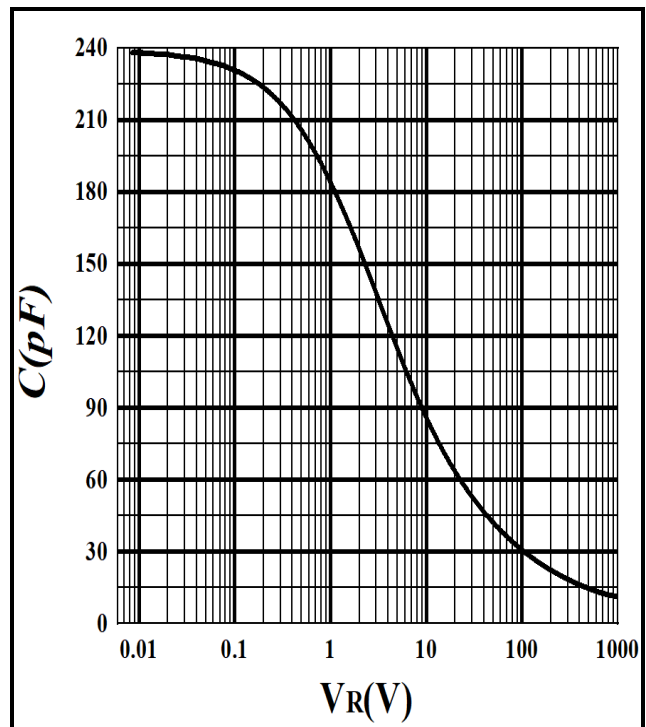


Figure 4. Capacitance vs. reverse voltage

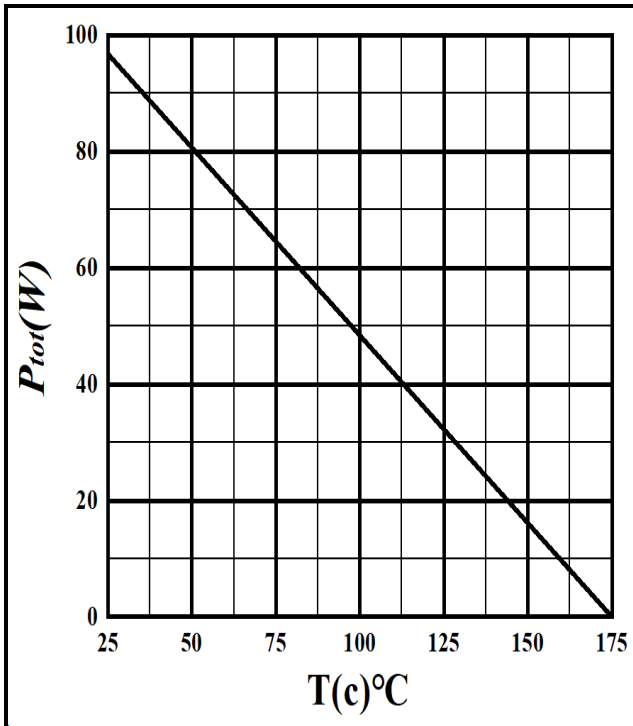


Figure 5. Power derating

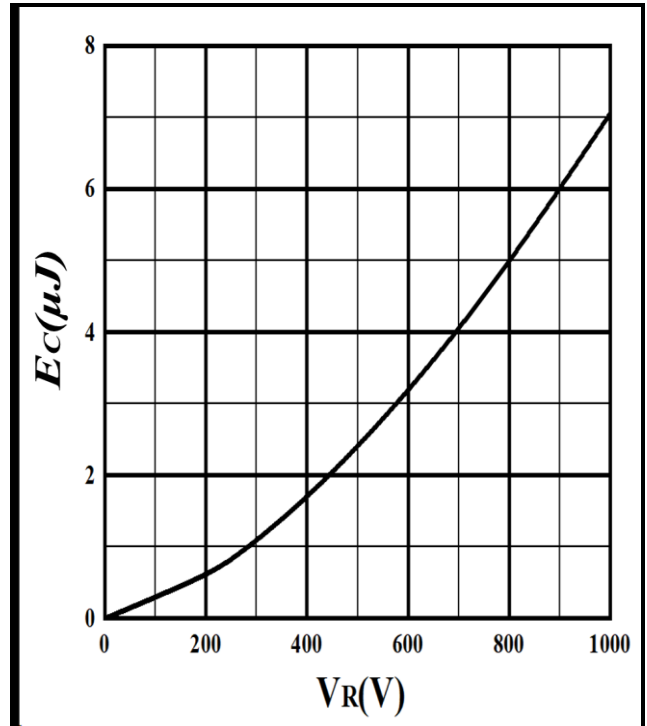


Figure 6. Capacitance stored energy

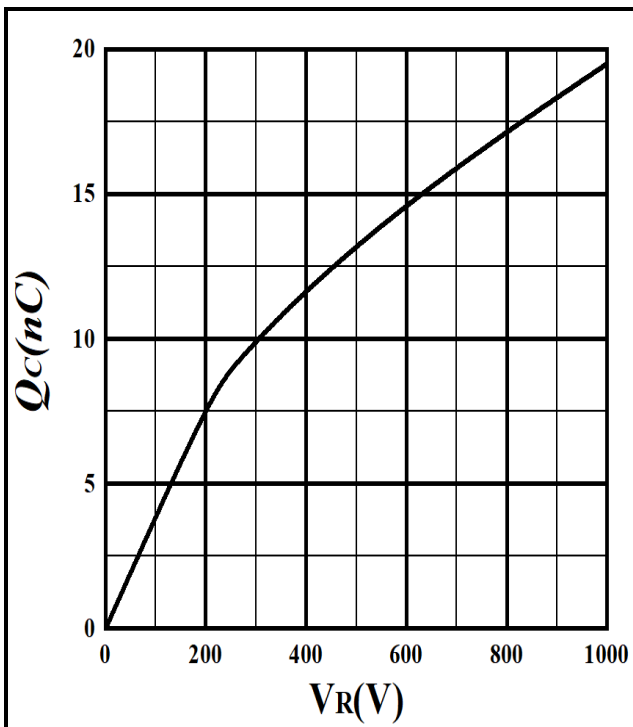


Figure 7. Total capacitance charge vs. reverse voltage

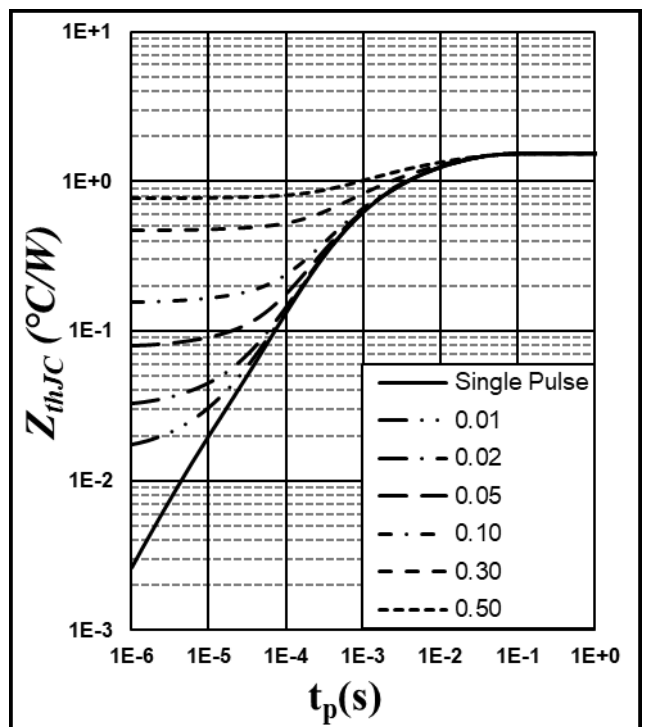
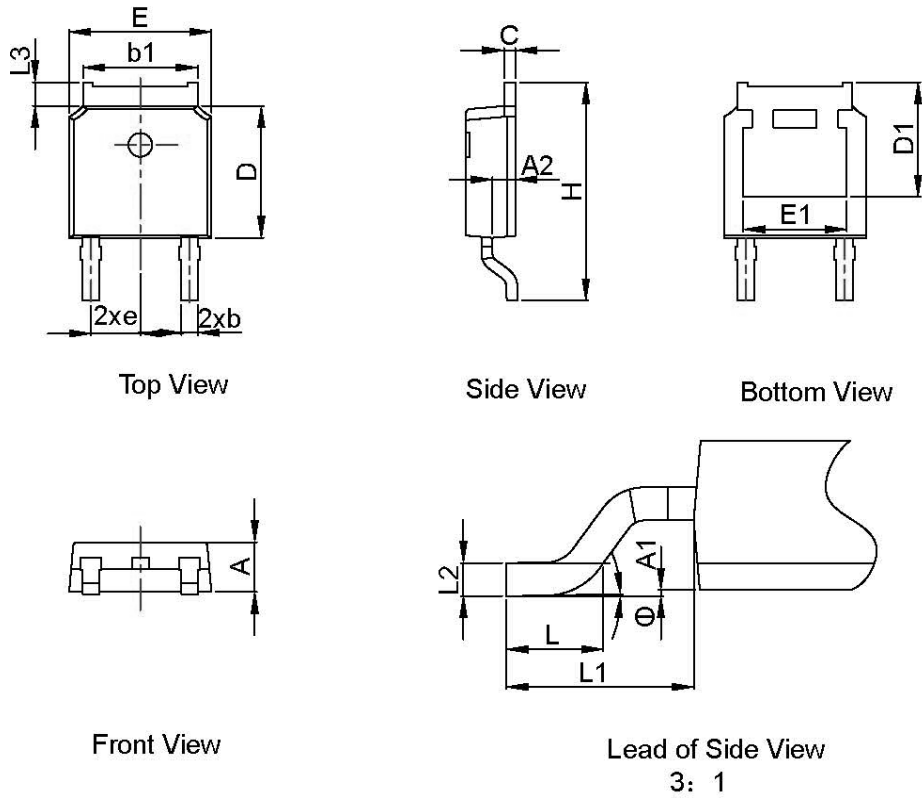


Figure 8. Transient Thermal Impedance
(Junction-Case)

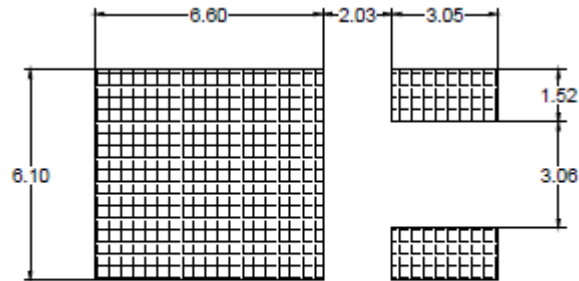
Package Information



Dimension unit: [mm]			
Symbol	Min	Nom	Max
A	2.20	2.30	2.38
A1	0	-	0.127
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b1	5.20	5.33	5.46
c	0.43	0.53	0.61
D	5.98	6.10	6.22
D1	5.30 REF		
E	6.40	6.60	6.73
E1	4.63	-	-
e	2.286 BSC		
H	9.40	10.10	10.50
L	1.38	1.50	1.75
L1	2.743 REF		
L2	0.51 BSC		
L3	0.88	-	1.28
θ	0°	-	8°

Recommended Solder Pad Layout

Note: All dimensions are in mm



TO-252-2L

Ordering Information

Part number	SDS120J003D3-ISARH
Package	TO-252-2L
Unit quantity	2500 EA
Packing type	Tape & Reel

Important Notices – Read Carefully

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