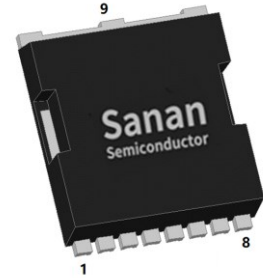


650V 35mΩ Silicon Carbide Power MOSFET

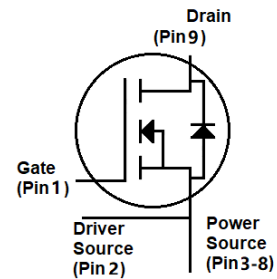
Features

- High blocking voltage with low on-resistance
- High switching speed with low capacitance
- Very fast and robust intrinsic body diode with low reverse recovery
- Very low switching losses
- Excellent avalanche ruggedness
- RoHS compliant



Benefits

- Greater system efficiency
- Reduced cooling requirements
- Increased power density
- Increased system switching frequency
- Easy to parallel and simple to drive



Potential Applications

- Solar inverters
- Uninterrupted power supplies
- Switch mode power supplies
- Motor drives

Package Type: TOLL



Description

The Sanan Semiconductor 650V/35mΩ silicon carbide power MOSFET uses advanced SiC MOSFET technology with low on-resistance, low switching losses, and a high operation temperature of 175°C. It is suitable for use in high frequency circuits and provides a reduction in overall system size, increased efficiency and increased switching frequency. It has been widely used in applications including solar inverters, uninterrupted power supplies, switch mode power supplies, and motor drives. Using RoHS compliant components, it is qualified for use in industrial application.

Product Specifications

Device	V _{DS}	I _D (25°C)	R _{(DS)on}	Marking
SMS0650035Y	650V	52A	35mΩ	MS0650035Y

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Table 1. Maximum Ratings

(T_C = 25°C, unless otherwise specified)

Parameter	Symbol	Value	Unit	Test conditions
Drain-source voltage	V _{DSmax}	650	V	V _{GS} = 0V, I _D = 100μA
Gate-source voltage, max. transient voltage	V _{GSmax}	-10/+25		t _p ≤ 0.5us, D < 1%
Gate-source voltage, max. static voltage	V _{GSmax}	-8/+22		
Gate-source voltage	V _{GSop}	-5/+18		Recommended operation values,
Continuous drain current	I _D	52	A	V _{GS} = 18V
		37		V _{GS} = 18V, T _C = 100°C
Pulsed drain current	I _{D(pulse)}	130	A	Pulse width t _p limited by T _{jmax}
Power dissipation	P _{tot}	202	W	
Operating junction temperature	T _j	-55~175	°C	
Storage temperature	T _{stg}	-55~175	°C	
Soldering temperature	T _L	260	°C	1.6mm from case for 10s

Table 2. Thermal Resistances

Parameter	Symbol	Values			Unit	Test condition
		Min.	Typ.	Max.		
Thermal resistance from junction to case	R _{th(j-c)}	/	0.62	/	°C/W	

Table 3. Static Electrical Characteristics

(T_j = 25°C, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Drain-source breakdown voltage	V _{(BR)DSS}	650	/	/	V	V _{GS} = 0V, I _D = 100μA
Gate threshold voltage	V _{GS(th)}	1.8	2.8	4.2		V _{DS} = V _{GS} , I _D = 6.5mA
		/	2.1	/		V _{DS} = V _{GS} , I _D = 6.5mA, T _j = 175°C
Drain-source leakage current	I _{DSS}	/	1	50	μA	V _{DS} = 650V, V _{GS} = 0V
Gate-source leakage current	I _{GSS}	/	1	250	nA	V _{GS} = 18V, V _{DS} = 0V
Drain-source on-state resistance	R _{DS(on)}	/	45	/	mΩ	V _{GS} = 15V, I _D = 22A
		/	35	46		V _{GS} = 18V, I _D = 22A
		/	49	/		V _{GS} = 18V, I _D = 22A, T _j = 175°C
Transconductance	g _{fs}	/	11	/	S	V _{DS} = 20V, I _D = 22A
		/	13	/		V _{DS} = 20V, I _D = 22A, T _j = 175°C
Internal gate resistance	R _{G(int)}	/	3.4	/	Ω	f = 1MHz, V _{AC} = 25mV

Table 4. Dynamic Electrical Characteristics

(T_j = 25°C, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Input capacitance	C _{iSS}	/	1616	/	pF	V _{GS} = 0V, V _{DS} = 600V, f = 1MHz, V _{AC} = 25mV
Output capacitance	C _{oSS}	/	144	/		
Reverse transfer capacitance	C _{rSS}	/	3.4	/		
C _{oSS} stored energy	E _{oSS}	/	30	/	μJ	
Gate to source charge	Q _{GS}	/	23	/	nC	V _{DD} = 400V, V _{GS} = -5/+18V, I _D = 22A, I _{GS} = 1mA
Gate to drain charge	Q _{GD}	/	19	/		
Total gate charge	Q _G	/	72	/		

Table 5. Switching Characteristics

(T_j = 25°C, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Turn-on delay time	t _{d(on)}	/	12	/	ns	V _{DD} = 400V, V _{GS} = -5/+18V, I _D = 22A, R _{G(ext)} = 4.7Ω, L = 110μH
Rise time	t _r	/	16	/		
Turn-off delay time	t _{d(off)}	/	28	/		
Fall time	t _f	/	8	/		
Turn-on switching energy	E _{on}	/	61	/	μJ	
Turn-off switching energy	E _{off}	/	34	/		

Table 6. Reverse SiC Diode Characteristics

($T_j = 25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Diode forward voltage	V_{SD}	/	4.1	/	V	$V_{GS} = -5\text{V}, I_{SD} = 22\text{A}$
		/	3.6	/		$V_{GS} = -5\text{V}, I_{SD} = 22\text{A}, T_j = 175^\circ\text{C}$
Continuous diode forward current	I_S	/	/	52	A	$V_{GS} = -5\text{V}, T_C = 25^\circ\text{C}$
Diode pulse current	$I_{S, pulse}$	/	/	130	A	$V_{GS} = -5\text{V}$, pulse width t_p limited by T_{jmax}
Reverse recovery time	t_{rr}	/	16	/	ns	$V_{GS} = -5\text{V}, I_{SD} = 22\text{A}, V_R = 400\text{V}, di/dt = 1.04\text{kA}/\mu\text{s}$
Reverse recovery charge	Q_{rr}	/	0.18	/	μC	
Peak reverse recovery current	I_{rrm}	/	18	/	A	

Electrical Characteristic Diagrams

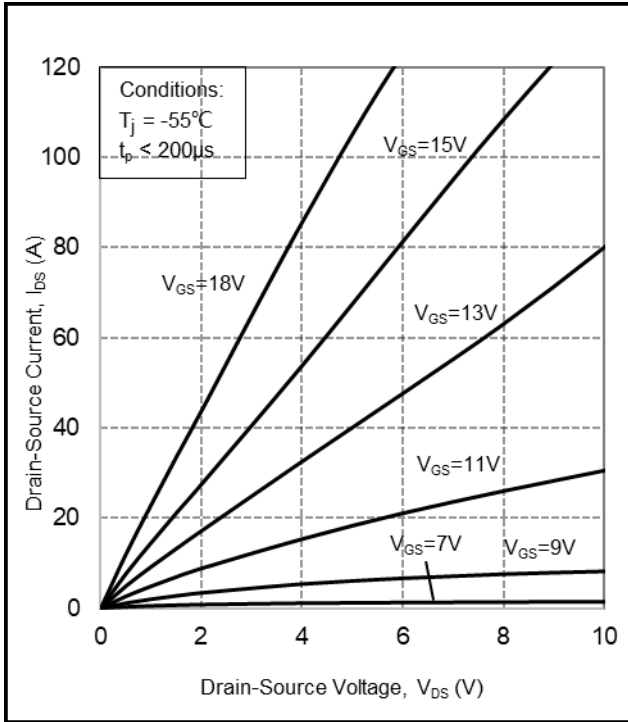


Figure 1. Output characteristics at $T_j = -55^\circ\text{C}$

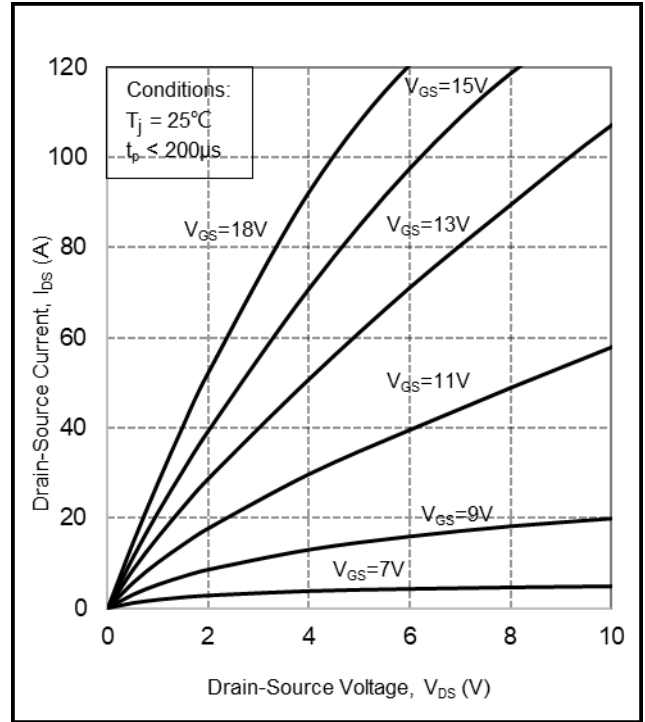


Figure 2. Output characteristics at $T_j = 25^\circ\text{C}$

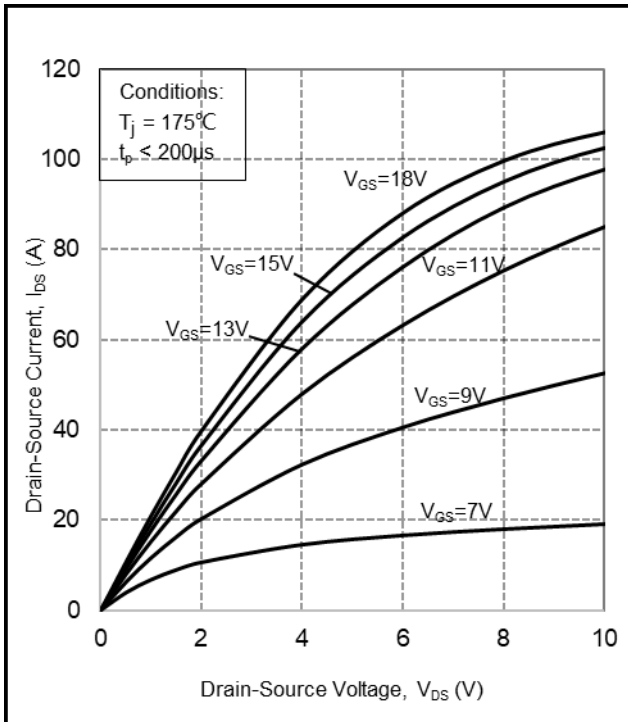


Figure 3. Output characteristics at $T_j = 175^\circ\text{C}$

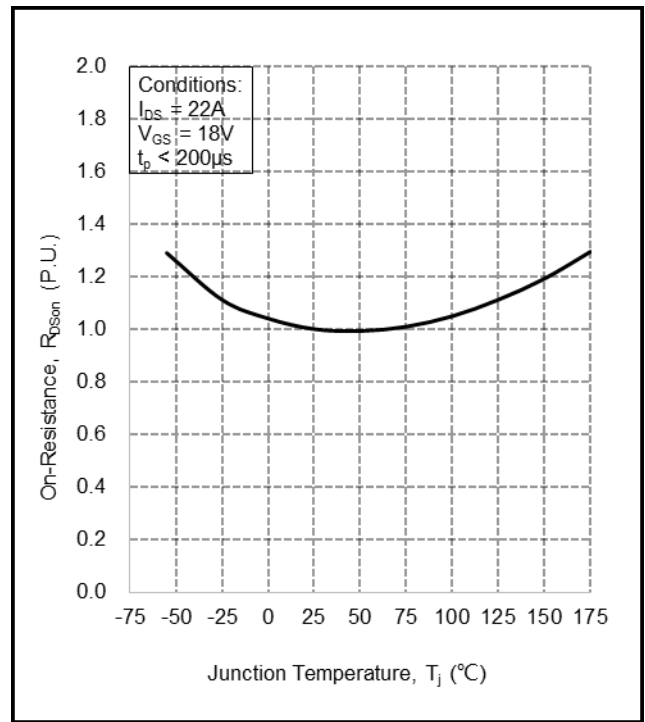


Figure 4. Normalized on-resistance vs. temperature

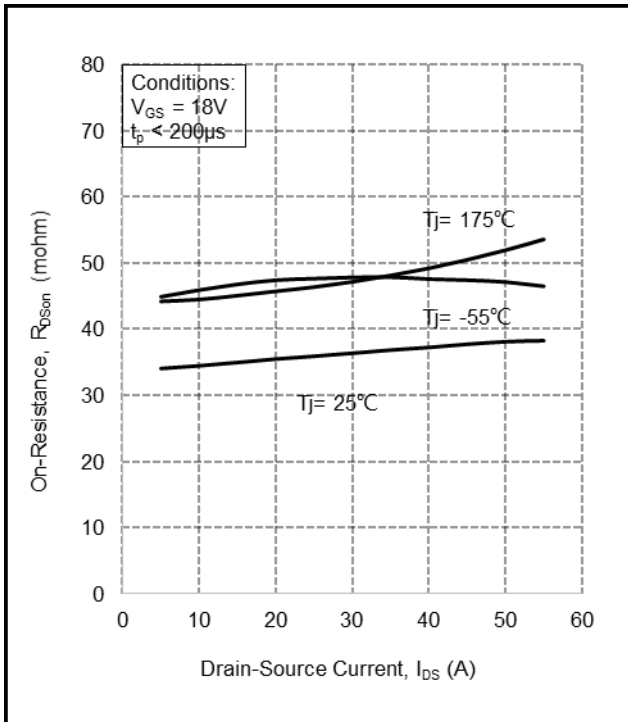


Figure 5. On-resistance vs. drain current for various temperatures

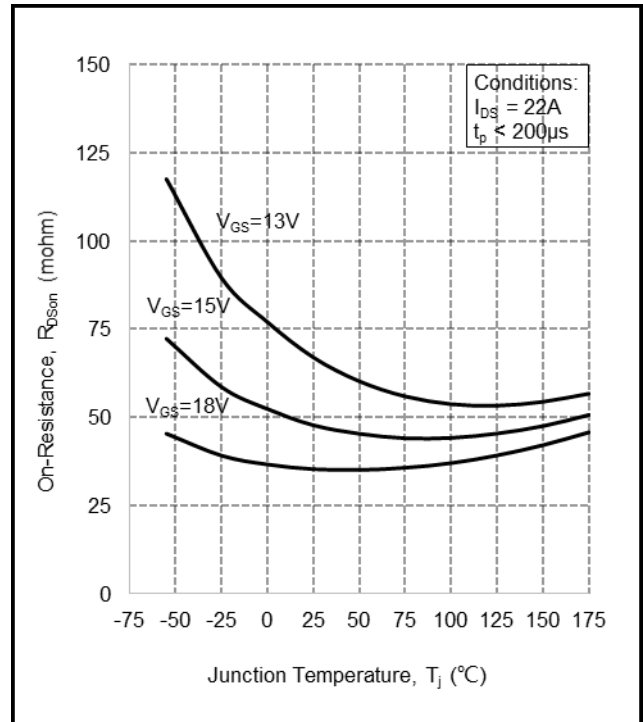


Figure 6. On-resistance vs. temperature for various gate voltages

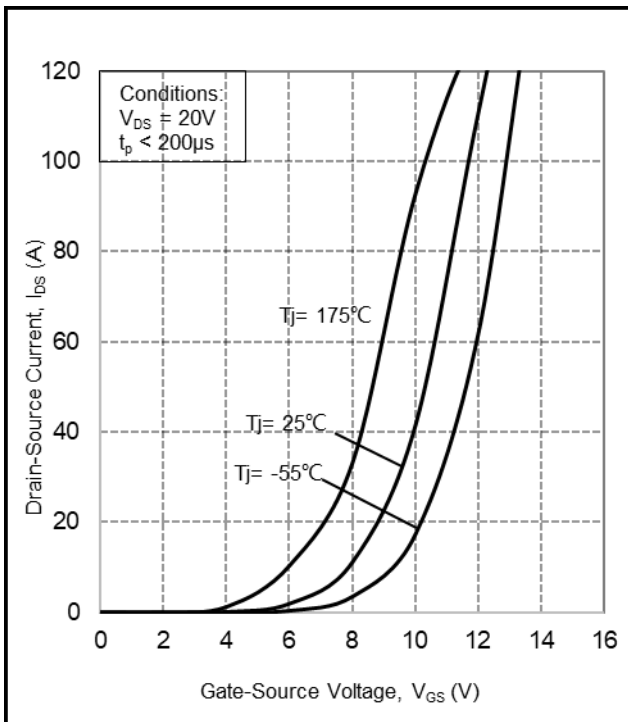


Figure 7. Transfer characteristic for various junction temperatures

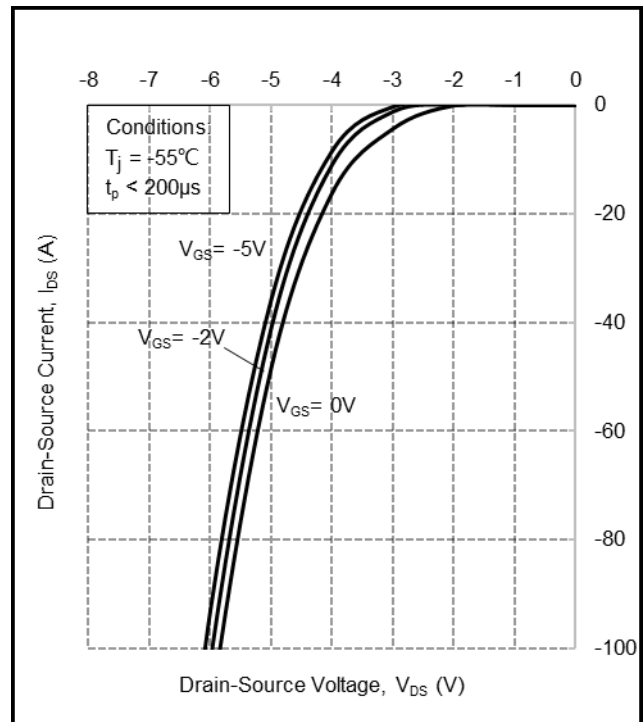


Figure 8. Body diode characteristic at $T_J = -55^\circ\text{C}$

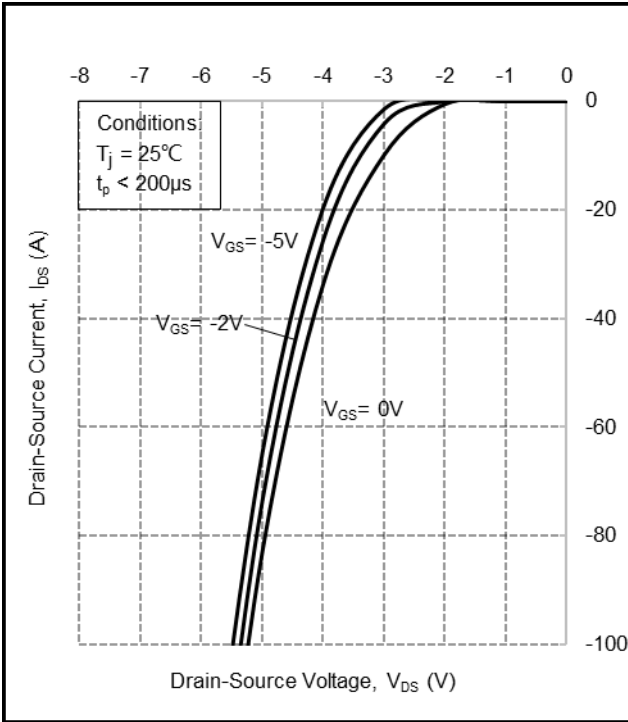


Figure 9. Body diode characteristic at $T_j = 25^\circ\text{C}$

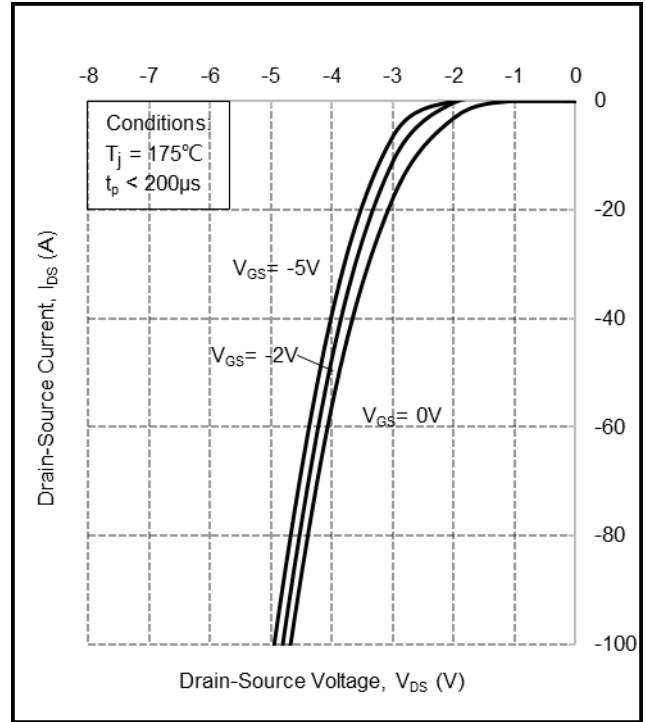


Figure 10. Body diode characteristic at $T_j = 175^\circ\text{C}$

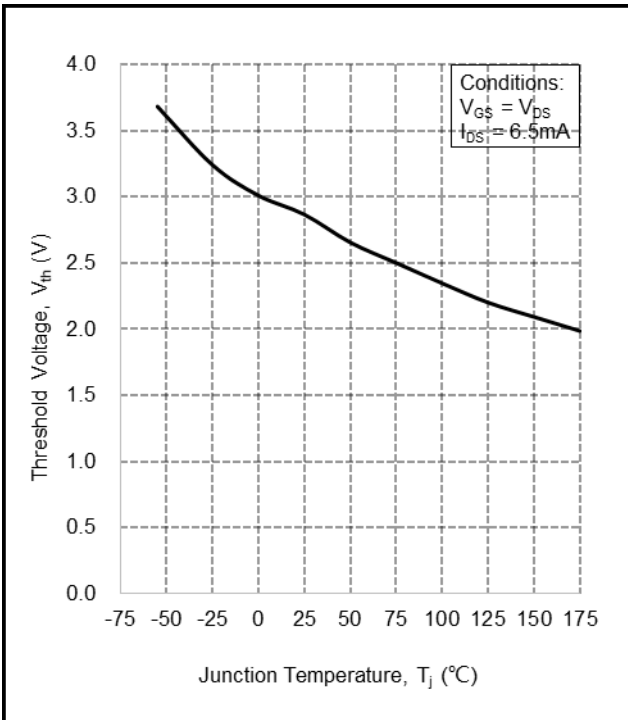


Figure 11. Threshold voltage vs. temperature

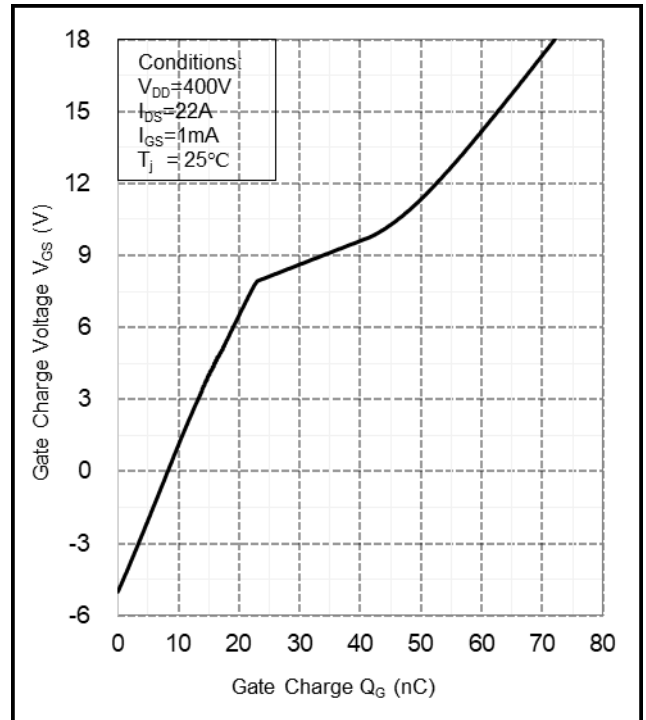


Figure 12. Gate Charge Characteristic

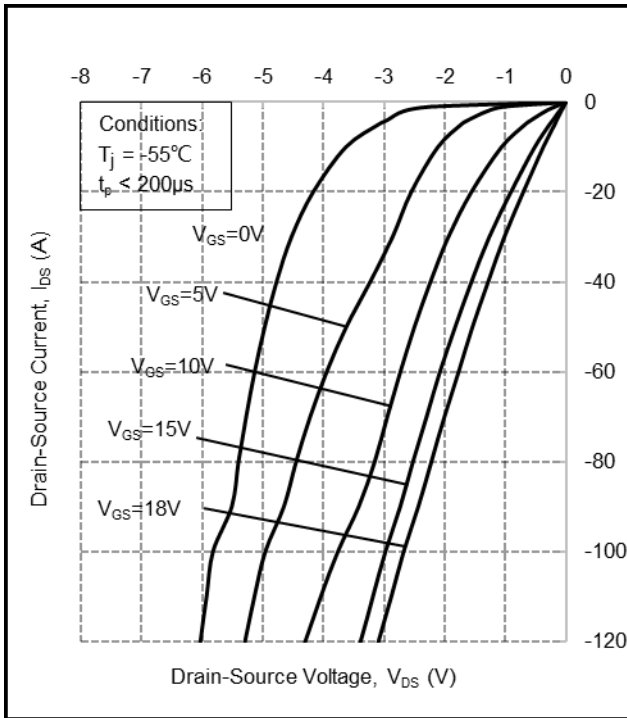


Figure 13. 3rd quadrant characteristic
at $T_j = -55^\circ\text{C}$

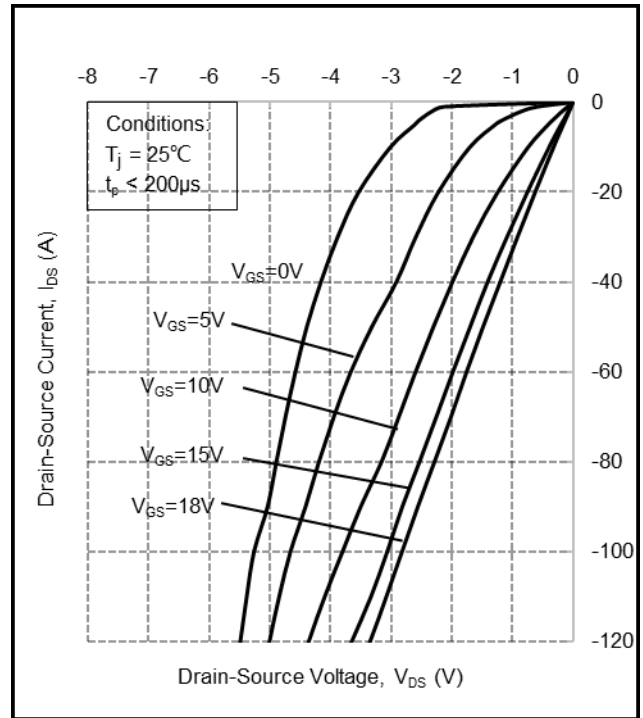


Figure 14. 3rd quadrant characteristic
at $T_j = 25^\circ\text{C}$

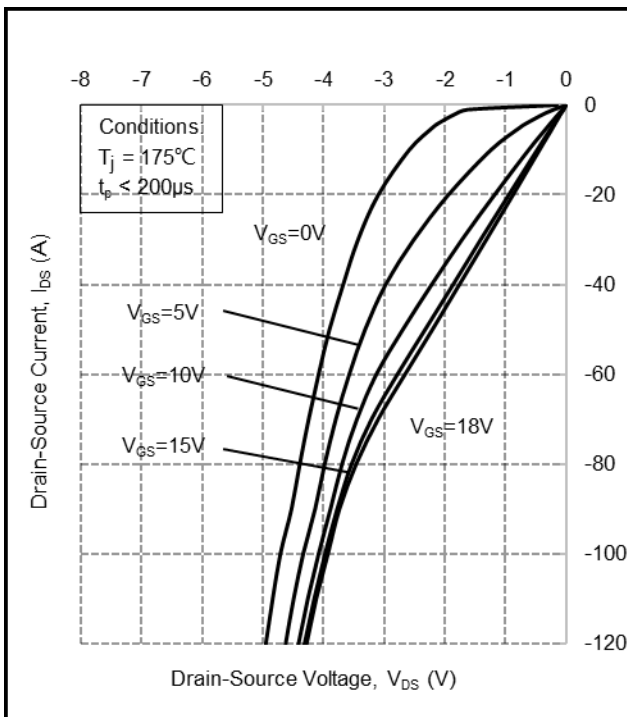


Figure 15. 3rd quadrant characteristic
at $T_j = 175^\circ\text{C}$

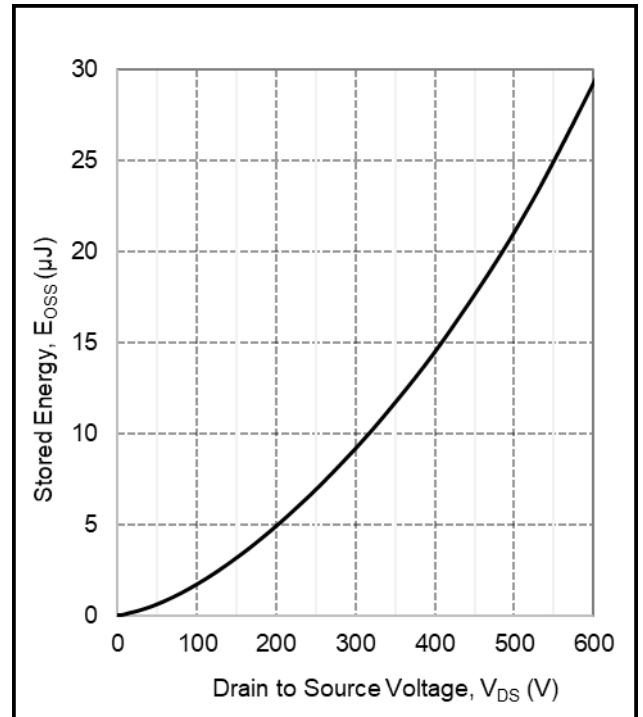


Figure 16. Output capacitor stored energy

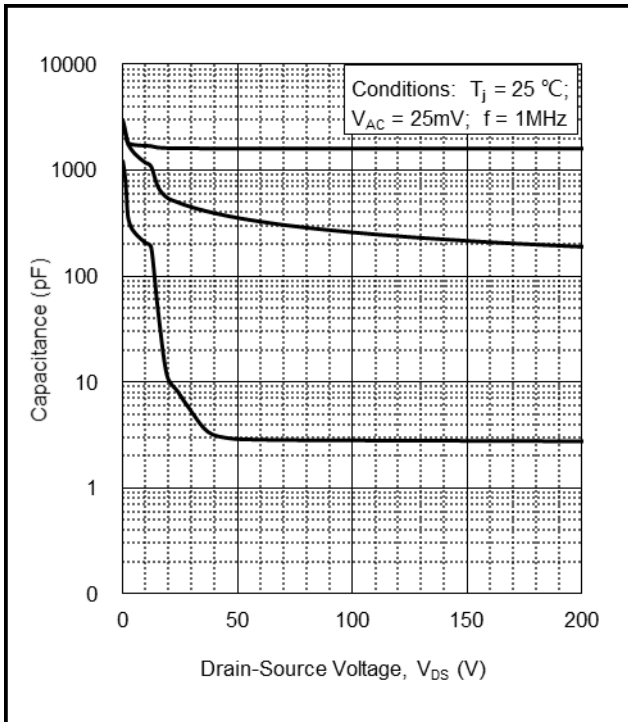


Figure 17. Capacitance vs. drain-source voltage (0 - 200V)

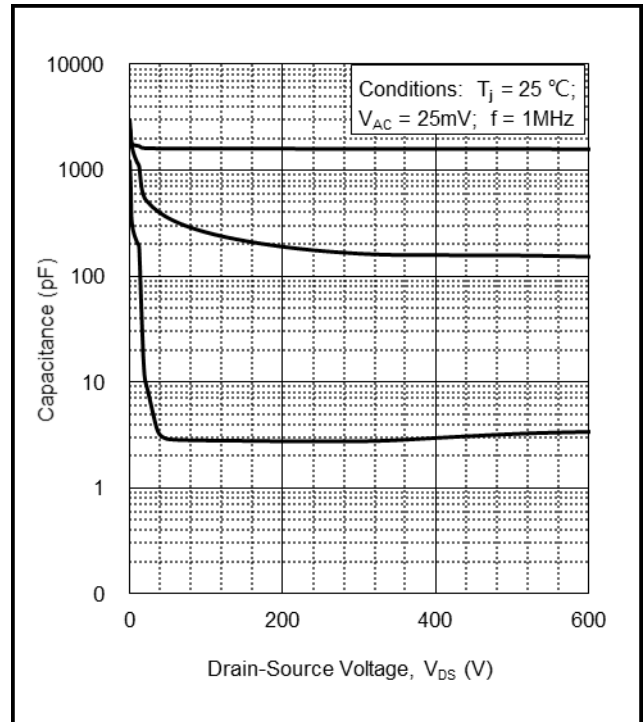


Figure 18. Capacitance vs. drain-source voltage (0 - 600V)

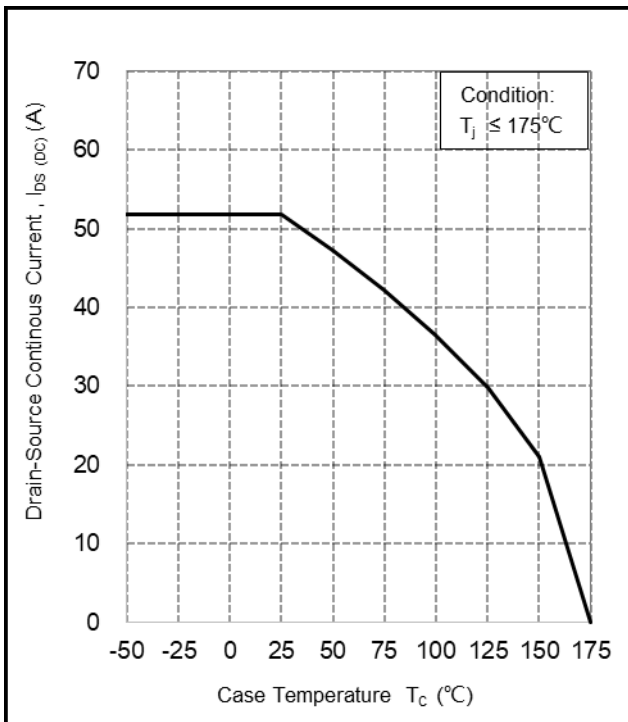


Figure 19. Continuous drain current derating vs. temperature

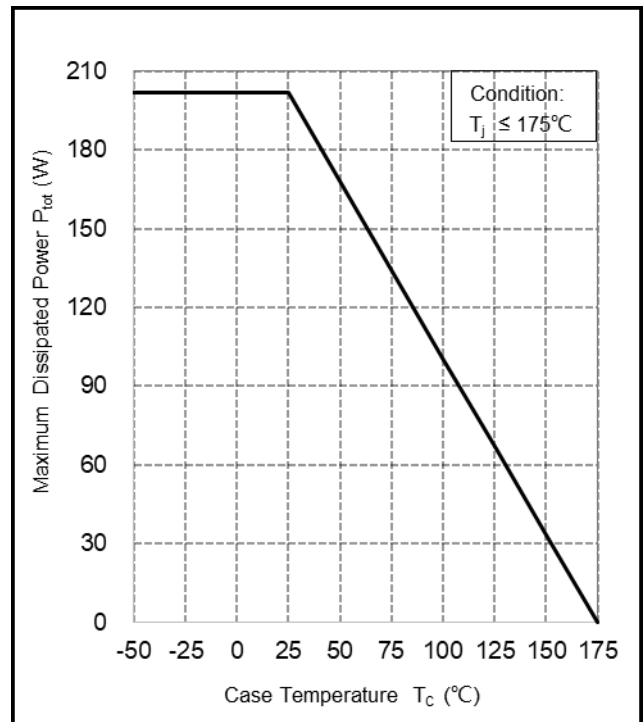


Figure 20. Maximum power dissipation derating vs. temperature

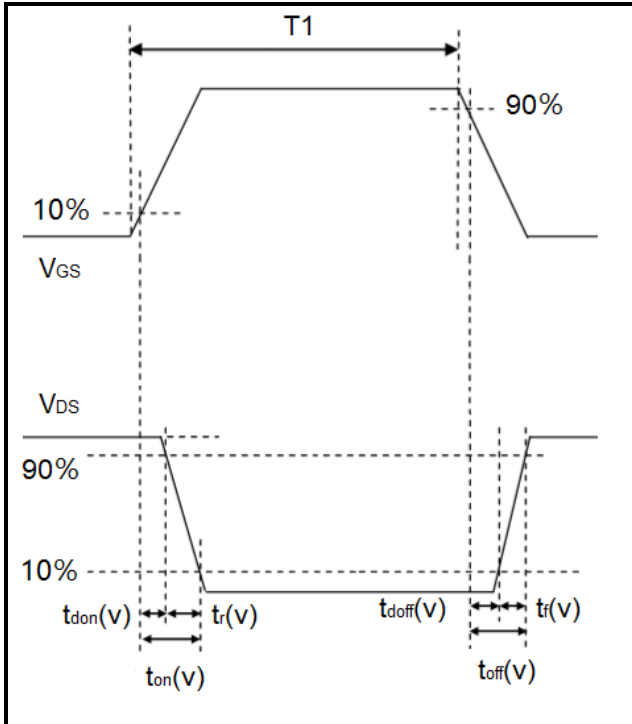


Figure 21. Switching times definition

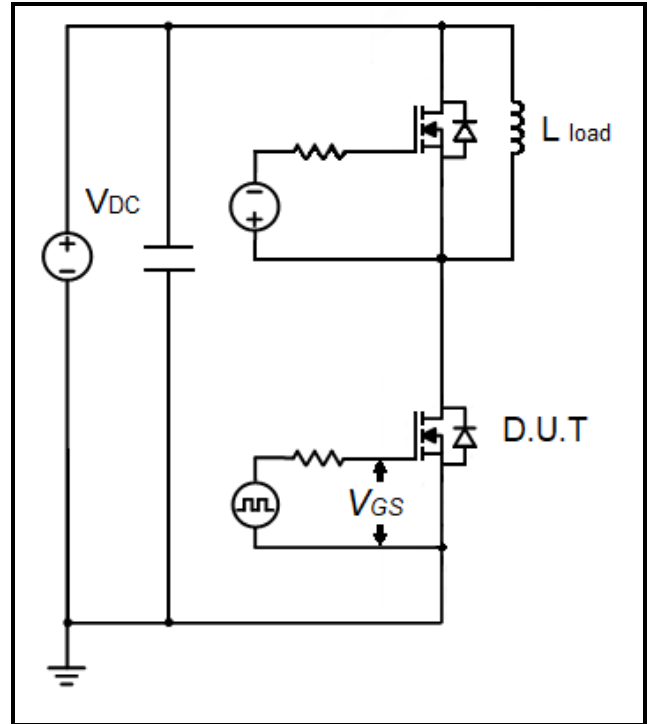
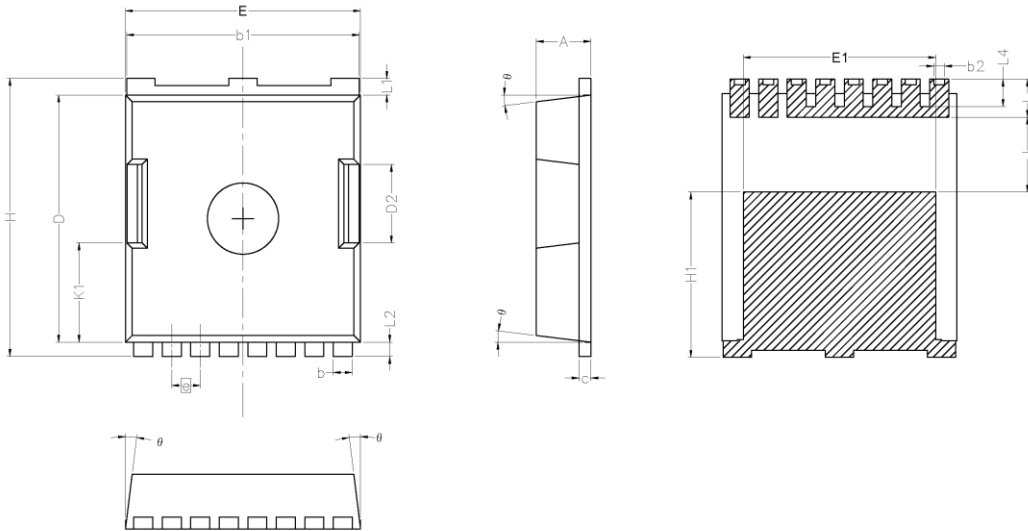


Figure 22. Clamped inductive switching waveform test circuit

Package Information

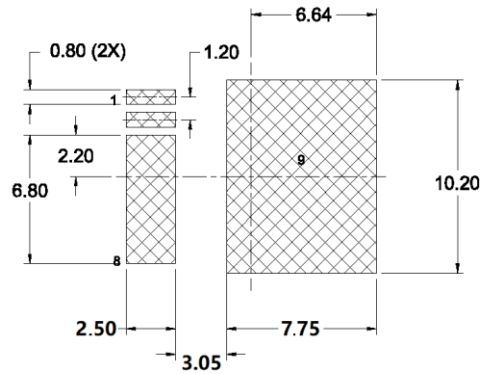


Dimension unit: [mm]		
Symbol	Min	Nom
A	2.20	2.40
b	0.70	0.90
b1	9.70	9.90
b2	0.42	0.50
c	0.40	0.60
D	10.28	10.58
D2	3.10	3.50
E	9.70	10.10
E1	7.90	8.30
e	1.20 BSC	
H	11.48	11.88
H1	6.75	7.15
N	8	
J	3.00	3.30
K1	3.98	4.38

Dimension unit: [mm]		
Symbol	Min	Nom
L	1.40	1.80
L1	0.60	0.80
L2	0.50	0.70
L4	1.00	1.30
θ	4°	10°

Recommended Solder Pad Layout

Note: All dimensions are in mm



TOLL

Ordering Information

Part number	SMS0650035Y-ISARH
Package	TOLL
Unit quantity	2000 EA
Packing type	Tape & Reel

Important Notices – Read Carefully

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