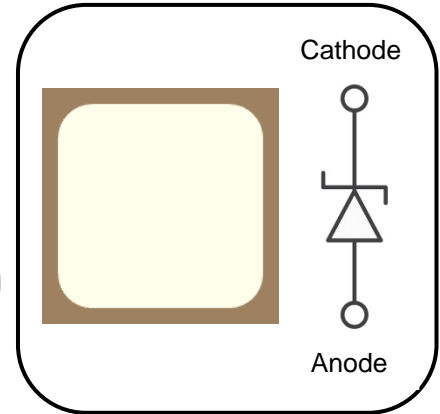


3rd Generation 650V/40A SiC Schottky Barrier Diode

Features

- AEC-Q101 qualified
- Revolutionary semiconductor material - Silicon Carbide (SiC)
 - No reverse recovery
 - High-speed switching performance
 - Temperature-independent switching behavior
 - System cost / size savings due to reduced cooling requirements
- Junction temperature range from -55°C to 175°C
- RoHS compliant



Applications

- DC/DC converter for EV/HEV
- On board charger (OBC)



Description

The ADS065J040B3 SiC Schottky Barrier Diode (SBD) has been developed using Sanan’s advanced 3rd generation SiC SBD technology with the highest performance and reliability. It registers higher efficiency, higher operation temperature and lower loss and can be operated at higher frequency than Si-based solutions. As to the Schottky structure, it shows no recovery at turn-off and allows a low leakage current with reverse voltage up to 650V. It can contribute to system miniaturization and achieve lightweight system design. Using RoHS compliant components and being AEC-Q101 qualified, it is qualified for use in automotive application.

Product Specifications

Device	V_{RRM}	$I_F (135^\circ C)$	$V_F (25^\circ C)$	Q_C
ADS065J040B3	650V	46A	1.35V	64nC

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Table 1. Maximum Ratings

(T_C = 25°C, unless otherwise specified)

Parameter	Symbol	Value	Unit	Test conditions
Repetitive peak reverse voltage	V _{RRM}	650	V	T _C = 25°C
Surge peak reverse voltage	V _{RSM}	650		T _C = 25°C
DC reverse voltage	V _{DC}	650		T _C = 25°C
Continuous forward current	I _F	90	A	T _C = 25°C
		46		T _C = 135°C
		40		T _C = 145°C
Surge non-repetitive forward current	I _{FSM}	270	A	T _C = 25°C, t _p = 10ms, half sine pulse
Repetitive peak forward current	I _{FRM}	192	A	T _C = 25°C, t _p = 10ms, half sine wave D = 0.1
i ² t value	∫i ² dt	364	A ² s	T _C = 25°C, t _p = 10ms
Operating junction temperature	T _j	-55~175	°C	
Storage temperature	T _{stg}	-55~175	°C	

Table 2. Thermal Resistance

Parameter	Symbol	Values			Unit	Test condition
		Min.	Typ.	Max.		
Thermal resistance from junction to case	R _{th(j-c)}	/	0.54	/	°C/W	

*Thermal Resistance is collected in SMBF

Table 3. Static Electrical Characteristics

(Tc = 25°C, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
DC blocking voltage	V _{DC}	650	/	/	V	I _R = 100 μA
Forward voltage	V _F	/	1.35	1.50	V	I _F = 40A, T _j = 25°C
		/	1.68	1.80		I _F = 40A, T _j = 175°C
Reverse current	I _R	/	4	80	μA	V _R = 650V, T _j = 25°C
		/	20	200		V _R = 650V, T _j = 175°C

Table 4. Dynamic Electrical Characteristics

(Tc = 25°C, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Total capacitance	C	/	2140	/	pF	V _R = 0V, f = 1MHz
		/	187	/		V _R = 200V, f = 1MHz
		/	158	/		V _R = 400V, f = 1MHz
Total capacitive charge	Q _C	/	64	/	nC	V _R = 400V
Capacitance stored energy	E _C	/	12.6	/	μJ	V _R = 400V

Electrical Characteristic Diagrams

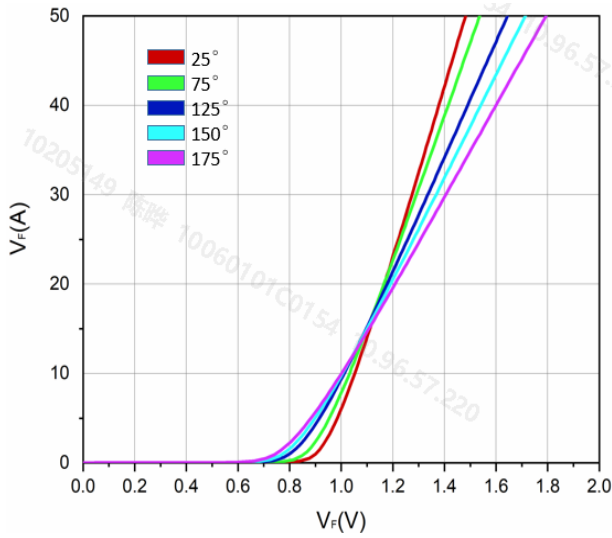


Figure 1. Forward characteristics

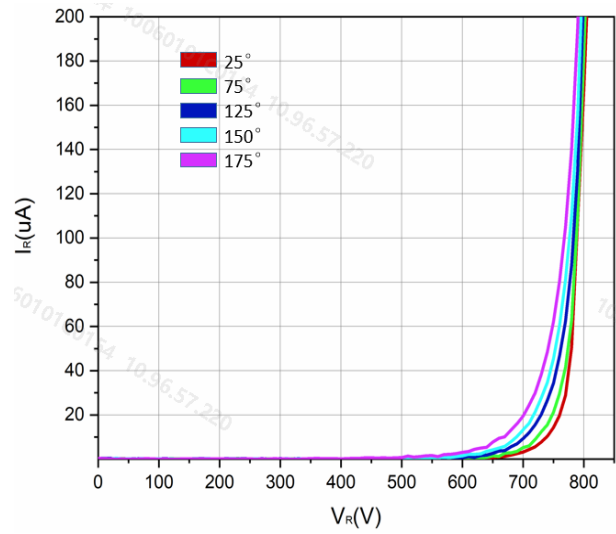


Figure 2. Reverse characteristics

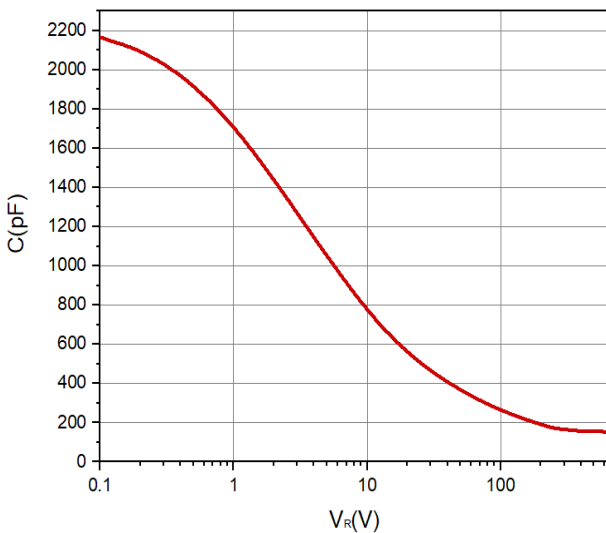


Figure 3. Capacitance vs. reverse voltage

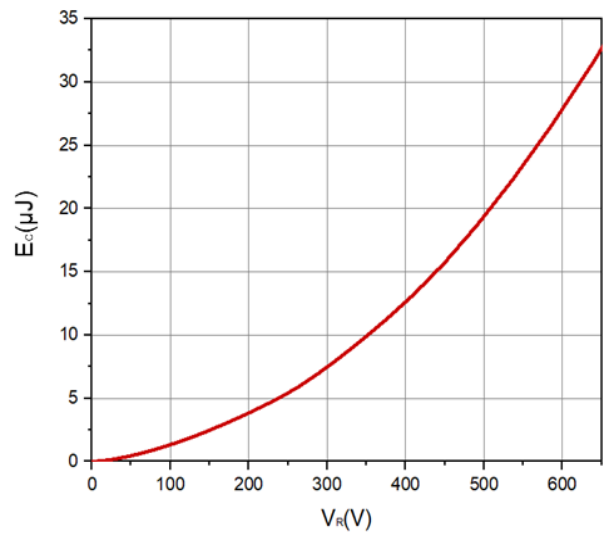


Figure 4. Capacitance stored energy

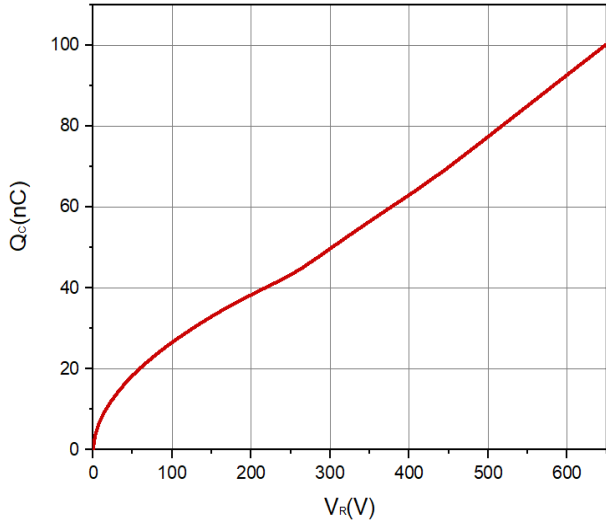


Figure 5. Total capacitance charge vs. reverse voltage

Ordering Information

Part number	ADS065J040B3
Package	Bare Die
Packing type	Wafer
RoHS	Yes

Important Notices – Read Carefully

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